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Power reduction in a multiprocessor digital signal processor based on processor load

CJ Nicol, KJ Singh - US Patent 6,141,762, 2000 - freepatentsonline.com

... in a multiprocessor digital signal processor based on processor load. ... so as to minimize overall power consumption. ... a multi-processor chip allocates tasks to the ...

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Load Balancing in Distributed Systems

TCK Chou, JA Abraham - IEEE Transactions on Software Engineering, 1982 - doi.ieeecomputersociety.org

... In order to fully utilize this diversity of processing power it is advantageous to assign ... This assignment of tasks to processors to maximize performance is ...

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[PDF] ► Adaptive load sharing in homogeneous distributed systems

DL Eager, ED Lazowska, J Zahorjan - IEEE Transactions on Software Engineering, 1986 - cs.usask.ca

... in the near future or the amount of processing that a particular task requires to ...

In attempting to fully exploit system processing power, a complex load ...

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Adaptive scheduling server for power-aware real-time tasks - ►ufsc.br [PDF]

P Mejia-Alvarez, E Levner, D Mossé - ACM Transactions on Embedded Computing Systems (TECS), 2004 - portal.acm.org

... be found in communication satellites, which have an extreme need for extending power management (for ... The utilization of a task is the processor load that it ...

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[BOOK] Real-time UML

BP Douglass - 1998 - Springer

... performance characteristics of the tasks allows a ... the tradeoffs of processor load balancing, communications ... fire control, avionics, nuclear power, life support ...

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A taxonomy of scheduling in general-purpose distributed computing systems -

► umanitoba.ca [PDF]

TL Casavant, JG Kuhl - IEEE Transactions on Software Engineering, 1988 - doi.ieeecs.org

... the manager ultimately has the power to decide ... tion and execution, where the tasks submitted by ... also achieves a certain degree of processor load balancing as ...

[Cited by 588](#) - [Related articles](#) - [Web Search](#) - [All 15 versions](#)

A heuristic method for reactive power planning

JRS Mantovani, AV Garcia, IS UNESP - Power Systems, IEEE Transactions on, 1996 - ieeexplore.ieee.org

... a bus of set Z, and the latter involving the amount of reactive power in such ... The investment subproblem will thus be called the master task, and the operation ...

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Minimizing memory utilization of real-time task sets in single and multi-processor systems-on-a-chip

P Gai, G Lipari, M Di Natale - Real-Time Systems Symposium, 2001.(RTSS 2001). Proceedings. ..., 2001 -

ieeexplore.ieee.org

... annealing-based tool that allows to find a near- optimal task allocation. ... architecture will not be able to support the needed computing power even taking into ...

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Compiler and Run-Time Support for Adaptive Load Balancing in Software Distributed Shared Memory ... - [►rochester.edu](#) (PDF)

S Ioannidis, S Dwarkadas - LECTURE NOTES IN COMPUTER SCIENCE, 1998 - Springer

... the amount of work assigned to each parallel task. ... of their power, giving us the equivalent of seven ... is only 9% slower than the seven processor load-free case. ...

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Adapting to load on workstation clusters - [►vast.ac.vn](#) (PDF)

RK Brunner, LV Kale - Frontiers of Massively Parallel Computation, 1999. Frontiers ..., 1999 - ieeexplore.ieee.org

... work to utilize the processing power of partially ... several advantages over other task migration schemes. ... permit fine control of processor load balance, allowing ...

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Key authors: [J Weissman](#) - [B Hamidzadeh](#) - [S Ioannidis](#) - [B Douglass](#) - [D Lilja](#)

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